

(43) Date of publication of application: **18.01.02**

H02M 7/48  
// H03K 7/08

(71) Applicant: MATSUSHITA ELECTRIC WORKS LTD

(72) Inventor: KITADO MASA HARU

COPYRIGHT: (C)2002.JPO

**SOLUTION:** A digital PWM control circuit 1B, including an error amplifier, of digital control type is composed of a subtracter 1Ba that calculates the difference between two input signals to the error amplifier portion for controlling the duty ratio of PWM output signals, a first register 1Bb that holds the result of subtraction from the subtracter 1Ba, a computing unit 1Bc that calculates incremental/decremental data to an adder 1Bd according to the value from the first register 1Bb, the adder 1Bd that adds up the result of computation from the computing unit 1Bc and the output data from the adder 1Bd, and a second register 1Be that holds pulse-width data, the output data of the adder 1Bd. A counter portion (B) counts the data in the second register 1Be on a constant-period clock; and thereby the PWM control circuit outputs PWM signals.